RTCU V3 Memory Architecture

 $\overline{(V3MA)}$

The RTCU V3 Memory Architecture (V3MA) is introduced to the X32-generation of RTCU products starting with firmware V3.00.

The V3MA is a new memory architecture that offers optimised memory usage and improved performance to support the evolution of future X32 firmware releases and RTCU applications. The V3MA architecture bridges the X32-generation with future RTCU generations by allowing a larger common code base to be realised which ultimately offers the most powerful, feature rich and flexible platform now and in the future.

The usable space for firmware is increased by 25% and at the same time the execution speed can be increased by up to 10%.

When upgrading to X32 firmware V3.00, the transition to V3MA is automatically performed, and in most cases this operation is fully transparant as full application compatibility has been maintained. For applications that uses voice messages some restrictions do apply.

The following information must be taken into account when transitioning to V3MA:

- ➤ Latest software releases must be used when using firmware >=V3.00:
 - > RTCU IDE 6.40 or later.
 - > RTCU Deployment Server 3.10 or later.
 - > RTCUProg V6.60 or later.
 - ➤ Communication Support Package (CSP) V1.50 or later.
- ➤ When upgrading from firmware <V3.00 to >V3.00, an intermediate upgrade to V3.00 is mandatory.
- ➤ Downgrading to past firmware <V3.00 is fully supported.
- Extremely large projects using X32-enhanced memory above 576KB is not supported.
- ➤ When upgrading/downgrading to/from V3.00, voice messages are lost and must be transferred again.
- For firmware >= V3.00, a background update of firmware will erase the voice messages.
- ➤ For firmware >=V3.00, voice messages cannot be transferred duing a pending background update.
- Future development of new firmware features will only be introduced under V3MA.

For additional support or questions:

Email: support@logicio.com